US

Title: MULTIPLEXER CIRCUIT FOR CONVERTING PARALLEL DATA INTO SERIAL DATA AT HIGH SPEED AND SYNCHRONIZING THE SERIAL DATA WITH A CLOCK SIGNAL
Inventor: Shunichiro MASAKI
App. No.: Unknown Docket No.: 100021-00134

Fig.1
(PRIOR ART)

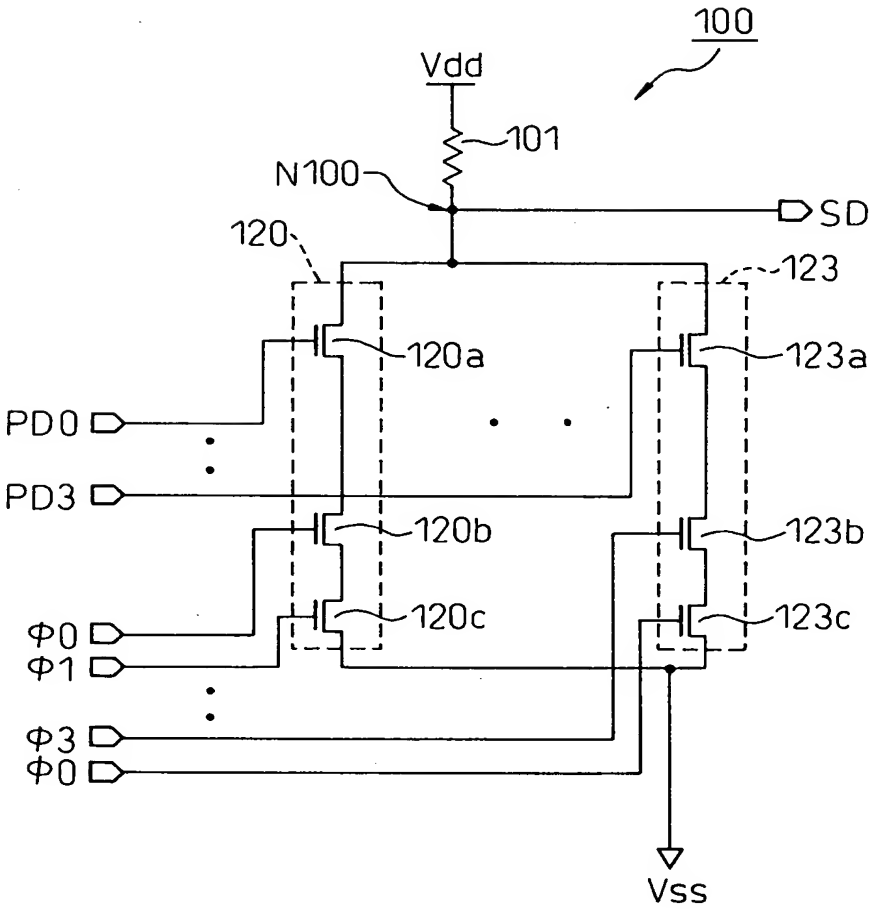

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Fig.2
(PRIOR ART)

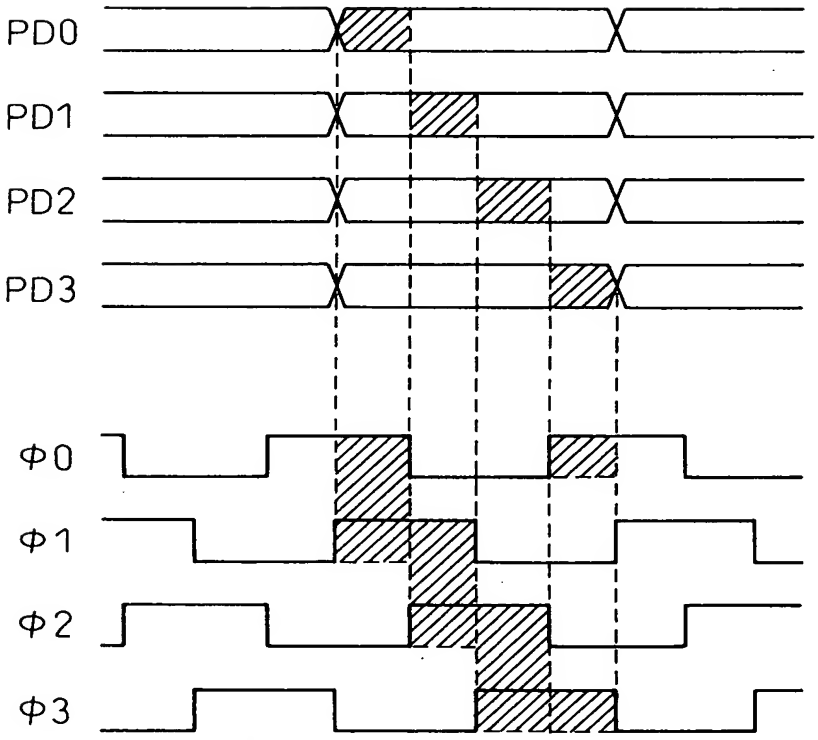

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Fig.3

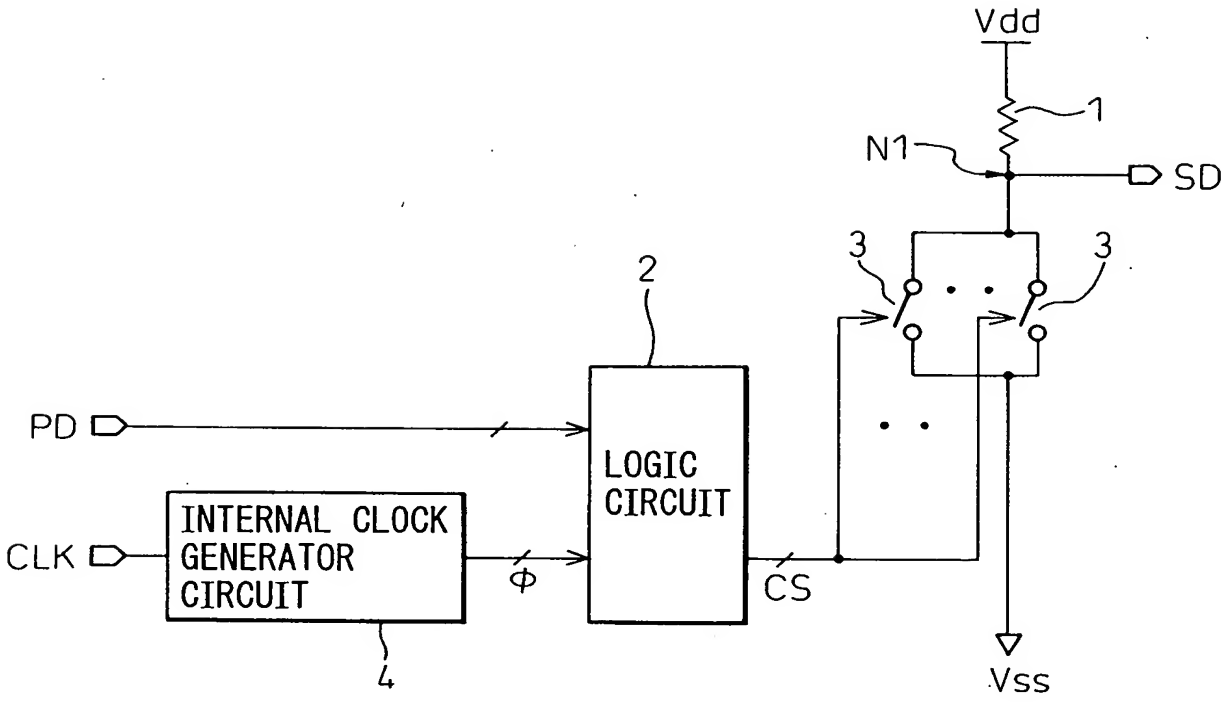

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Fig.4

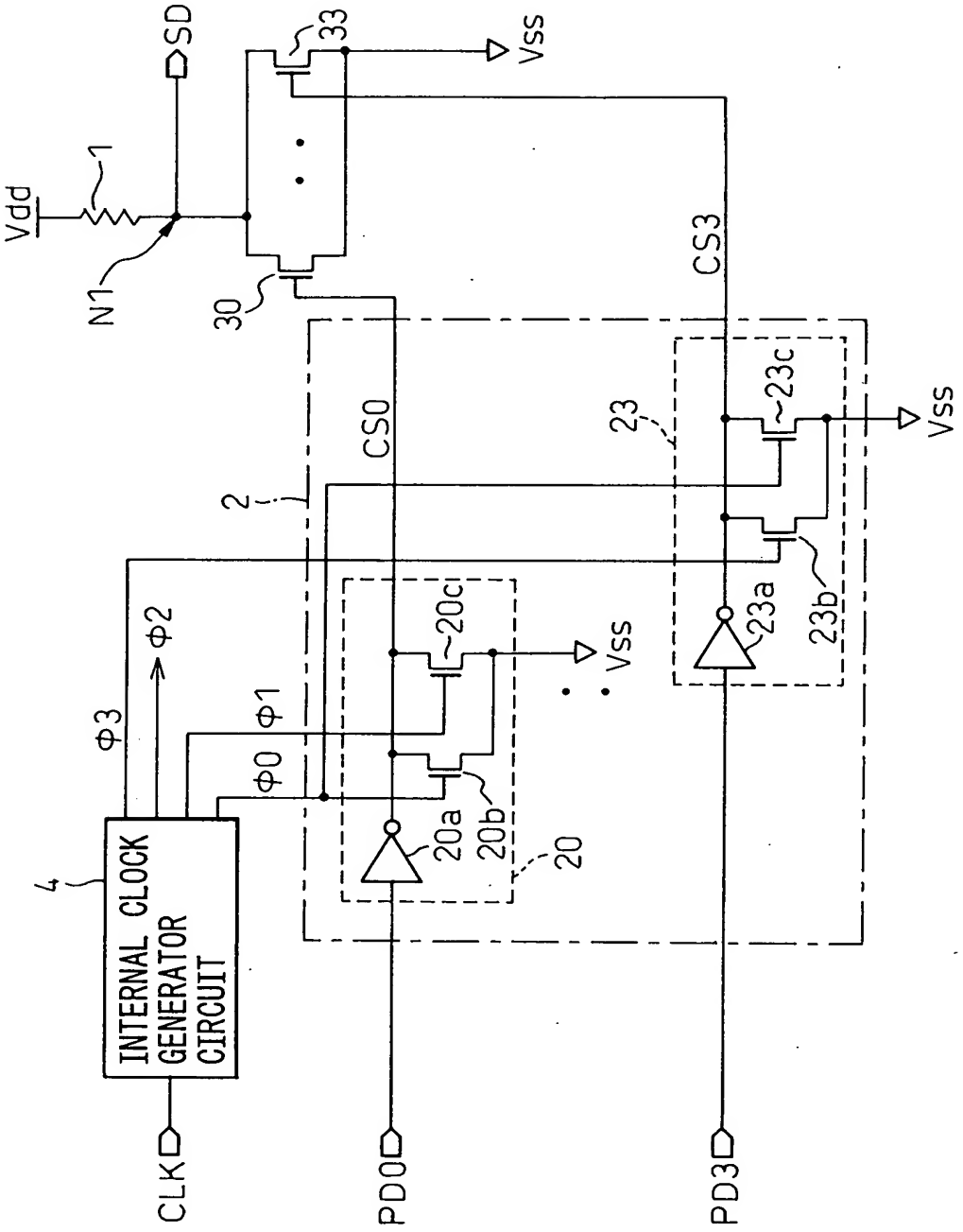

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Fig.5

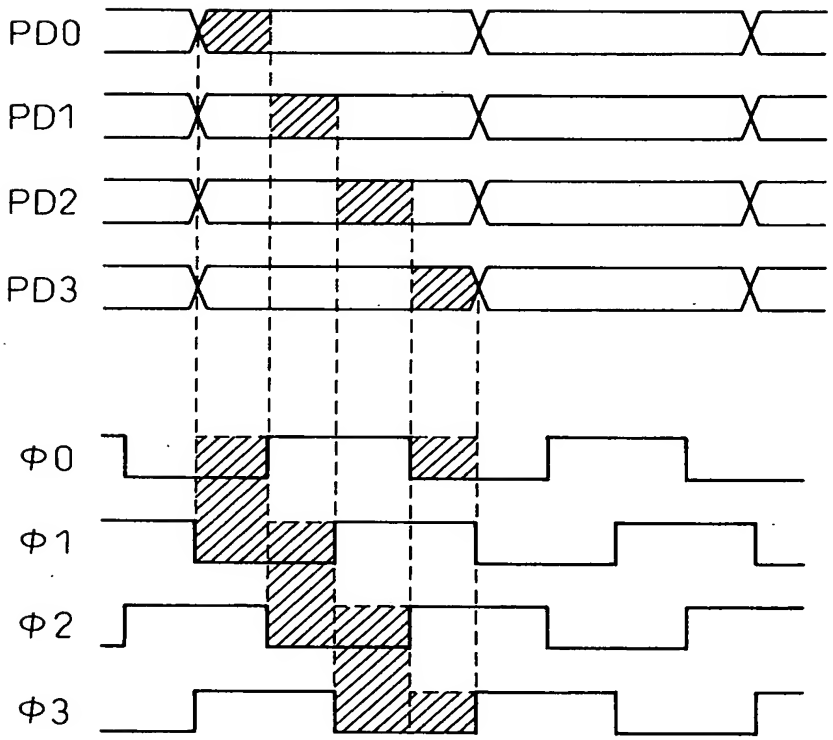

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Fig.6

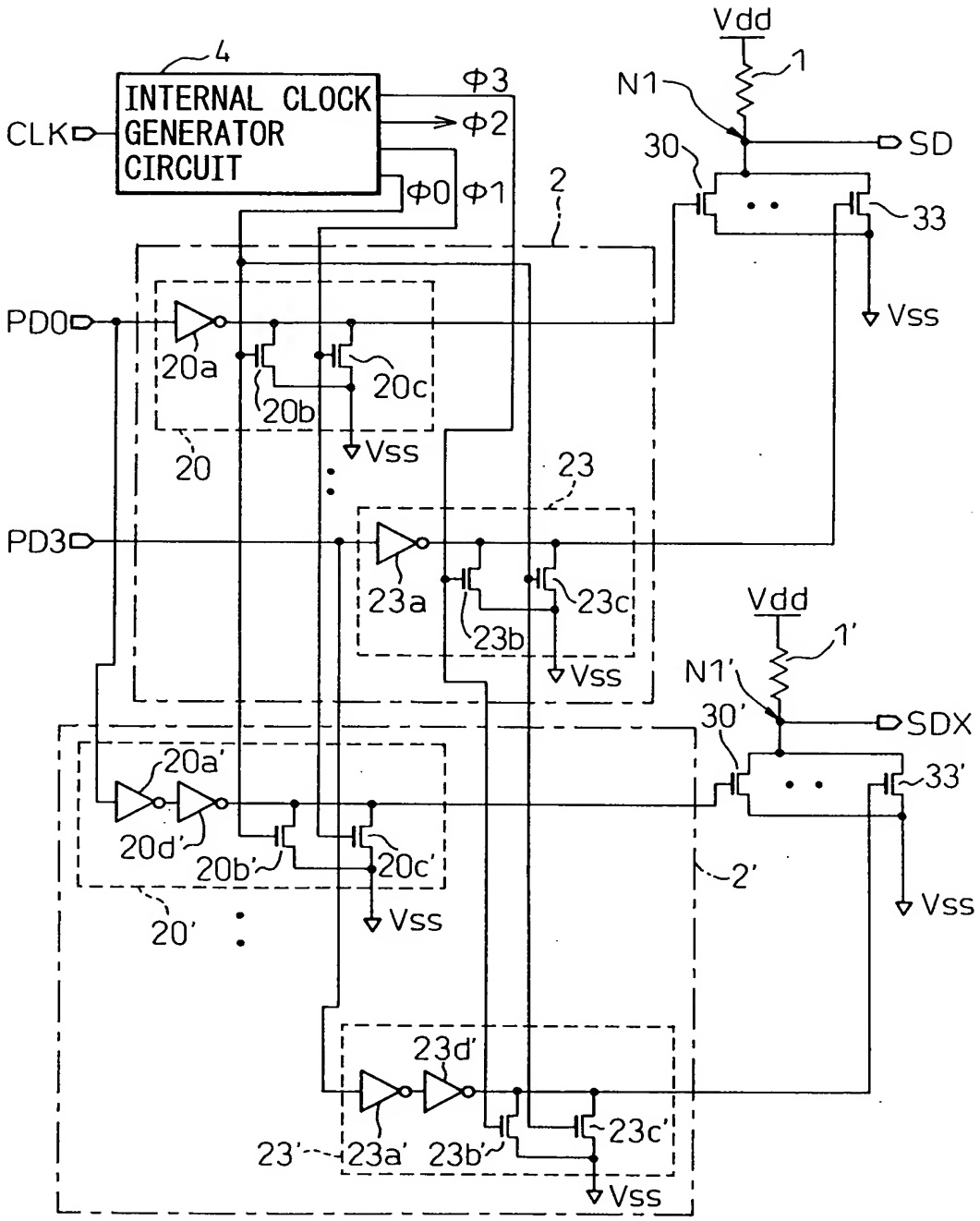

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Fig.7

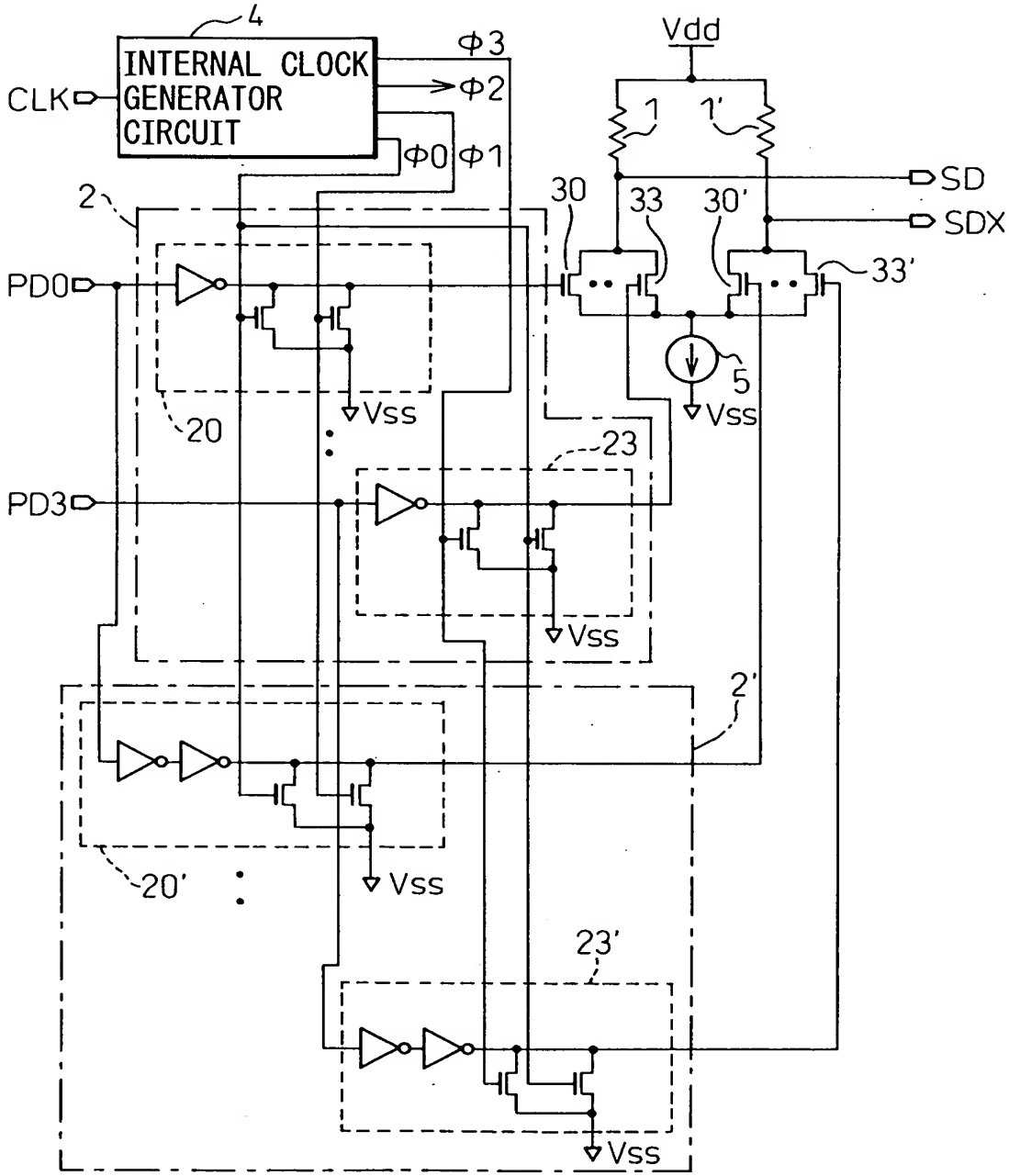

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Fig.8

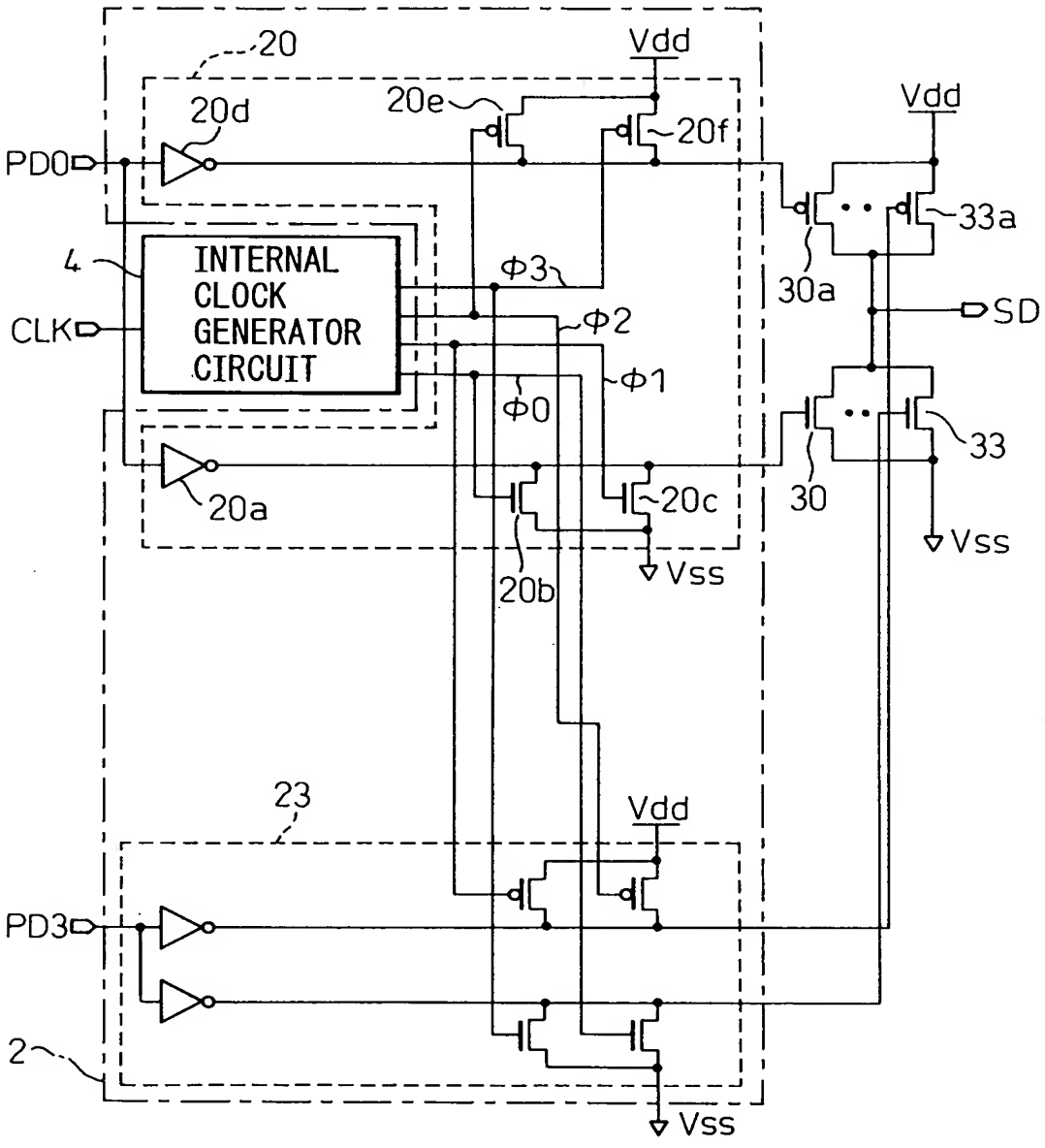

Fig.9

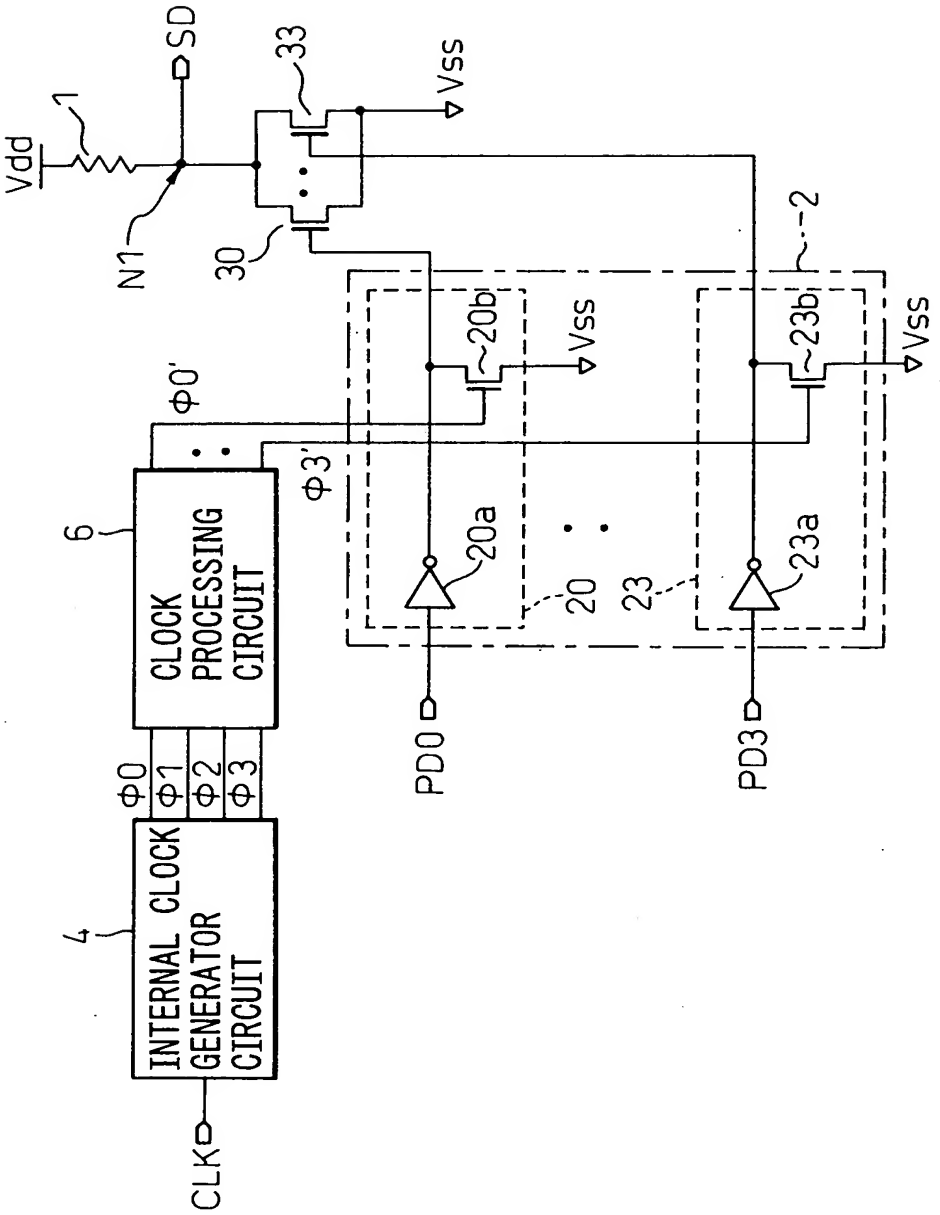
SD
33
Vss
Vdd
1
N1
30
2
20b
Vss
23b
Vss
Φ0'
Φ3'
20a
20
23
23a
6
CLOCK PROCESSING CIRCUIT
Φ0
Φ1
Φ2
Φ3
4
INTERNAL CLOCK GENERATOR CIRCUIT
CLK
PD0
PD3

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Fig.10

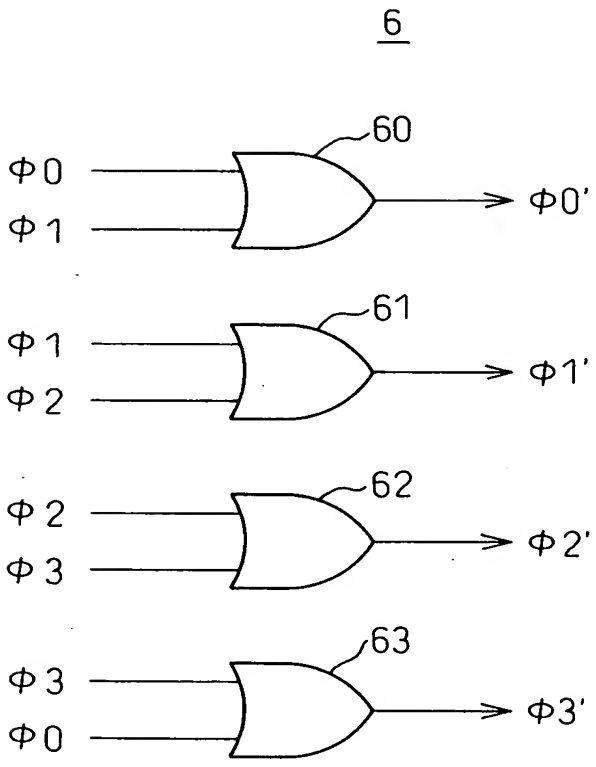

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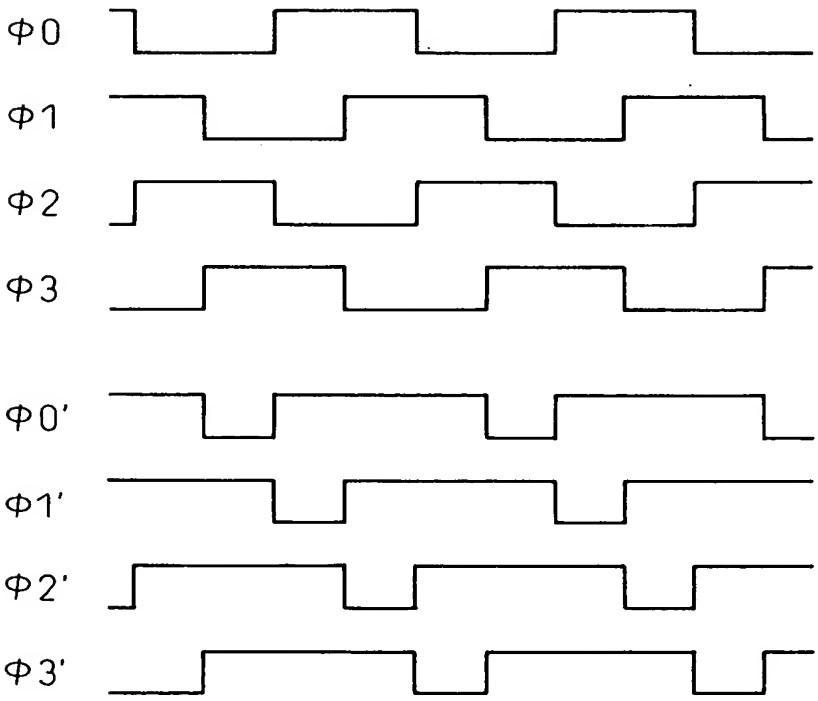
Fig.11



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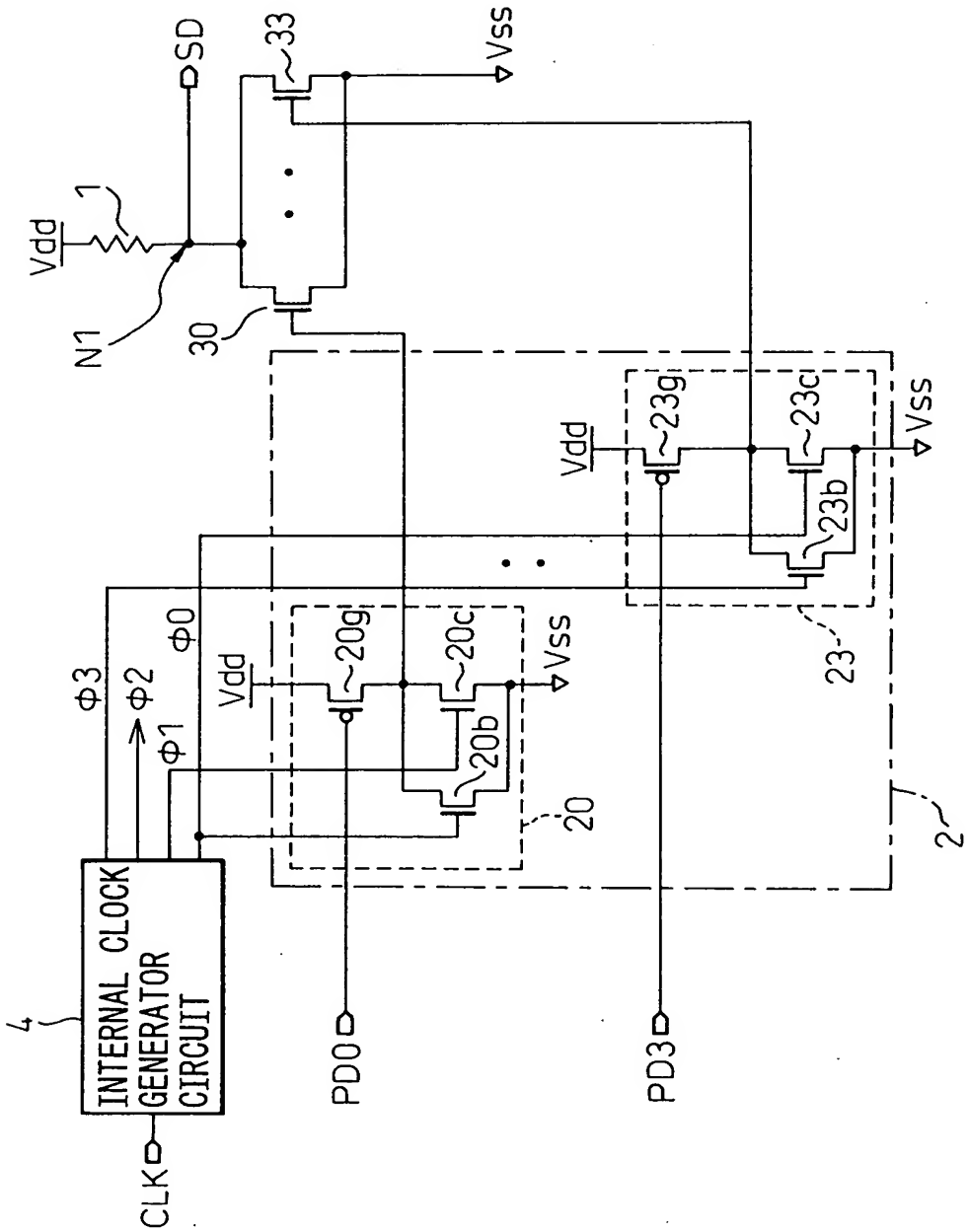
Fig.12